

IN THE SPECIFICATION

Please amend the specification as follows:

Page 1, line 16, change "accident after an accident"
to --accident and after an accident--.

Page 2, line 23, change "which compares" to --which
comprises--.

Page 5, line 46, change "tore" to --store--.

Page 7, line 22, change "data continued" to --data
contained--.

Page 15, line 33, change "datat" to --data--.

Page 17, line 1, change "16" to --4--.

IN THE CLAIMS

Please cancel restricted claims 22-31 without
prejudice or disclaimer.

Please amend claims 1-21 as follows:

Sub B1
Com 1: (Amended) A [video] recording device for capturing
[video frame] data [in response to a trigger event], said
recording device comprising:

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cont [at least one image sensor operative to produce an
output frame data signal representative of video images
impinging said at least one sensor;]

[a central control processor having inputs for
receiving video frame data and an input for receiving a
trigger event;]

[a converter coupled between said image sensor and
said central control processor and operative to convert
said output frame data signal and to communicate said video
frame data signal to said central control processor;]

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[a] at least one [semiconductor] memory [employed as a circular buffer memory, said semiconductor memory in electrical communication with said central control processor, said semiconductor memory having a plurality of locations] for storing [a corresponding plurality of frames comprising frame] data [corresponding to portions of said video frame data signal] associated with a time period;

[said central] a control processor [being] operative to store [respective frames] the data in [successive ones of said locations of said circular buffer] the at least one memory [in the absence of said trigger signal and to store respective frames only in a predetermined number of successive locations of said circular buffer memory] such that the stored data associated with a portion of the time period closer to an event has a first resolution and the stored data associated with a portion of the time period further from the event has a second resolution different than the first resolution.

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2. (Amended) The [video recording] device of claim 1, wherein said device further [includes] comprises:

at least one first sensor type operative to generate the data; and

[a] at least one second sensor type operative to [produce said] generate a [trigger] signal representing the event;

wherein the control processor operates to store the data such that the stored data associated with the portion of the time period closer to the event has the first resolution responsive to the signal.

3. (Amended) The [video recording] device of claim 2,

wherein said at least one second sensor type [comprises]
includes an accelerometer.

4. (Amended) The [video recording] device of claim 1,
[wherein said device] further [includes] comprising:

a capture switch;

[and said device] wherein the control processor is
operative to store only a predetermined [number of frames]
amount of data within said [semiconductor] memory following
user activation of said capture switch.

5. (Amended) The [video recording] device of claim 1,
wherein:

[said device is further operative to store data
representative of a single video frame within said
semiconductor memory upon user activation of a switch] the
data is video data representing a plurality of frames; and

the processor is operative such that a number of the
plurality of frames per a unit of time represented by the
stored video data associated with the portion of the time
period closer to the event is greater than a number of the
plurality of frames per the unit of time represented by the
stored video data associated with the portion of the time
period further from the event.

6. (Amended) The [video recording] device of claim 1,
wherein said [central] control processor is further
operative to compress [video frame] the data associated
with the portion of the time period closer to the event at
a first compression ratio and to compress the data
associated with the portion of the time period further from
the event at a second compression ratio different than the

first compression ratio, prior to the storage of [respective frames] the data within said [semiconductor] memory.

Sub B2 7. (Amended) The [video recording] device of claim 1, wherein [said central control processor is capable of storing frames in said semiconductor memory at a first frame rate and a second frame rate,] said processor [being] is further operative to store [frames] the data in said [semiconductor] memory at [said] a first [frame] rate prior to [said trigger] the event and [being operative to store said frames in said semiconductor memory] at [said] a second [frame] rate subsequent to [said trigger] the event.

AI cont 8. (Amended) The [video recording] device of claim [7] 1, wherein said second [frame rate] resolution is [greater] less than said first [frame rate] resolution.

9. (Amended) The [video recording] device of claim [7] 1, wherein said [central] control processor is operative to store [frame] the data only in approximately one-half the [number of said plurality of frame locations contained in said semiconductor] memory following [receipt of said trigger signal] the event.

10. (Amended) The [video recording] device of claim 1, further comprising:

[wherein said video recording device includes plural] a plurality of [image] sensors [coupled to said central control processor through said converter and] each [sensor is] operative to [produce] generate a respective [output

frame] portion of the data [signal representative of video images impinging the respective sensor,];

wherein said at least one [semiconductor] memory is [structured as] a plurality of [circular buffer] memories corresponding in number to said plurality of [image] sensors;

[and] wherein said [central] control processor is operative to [continue to] store [frames representative of the respective video images impinging] the respective portions of data generated by each [respective ones] of said [image] plurality of sensors in [successive ones of said frame locations of] a respective one of said plurality of [circular buffer] memories [in the absence of said trigger signal and to store frames only in a predetermined number of successive locations of said circular buffer memories following receipt of said trigger signal].

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11. (Amended) The [video recording] device of claim 1, further comprising:

[wherein said] an image sensor, [comprises] including a charge coupled device, operative to generate the data.

12. (Amended) The [video recording] device of claim 11, [wherein said device] further [comprises] comprising:

a lens [selectively] positioned so as to focus [a video] an image on said image sensor [and] to cover a [predetermined] viewing angle.

13. (Amended) The [video recording] device of claim 1, further comprising:

[wherein said] an image sensor, including [comprises] an artificial retina, operative to generate the data.

14. (Amended) The [video recording] device of claim 13,
[wherein said device] further [comprises] comprising:

a lens [selectively] positioned so as to focus [a
video] an image on said [image] artificial retina [and] to
cover a [predetermined] viewing angle.

Sub B3 15. (Amended) A method for recording [video frames] data,
[in response to a trigger signal] comprising the steps of:

[receiving at an image sensor, video frame images and
producing an image sensor output signal representative of
said received video frame images;]

[converting said image sensor output signal to digital
video signals, wherein a predetermined number of said video
signals comprise a frame;]

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cont [continuing to store] storing first data
[corresponding to successive frames in a semiconductor
buffer memory in the absence of the detection of said]
associated with a time period closer to an [trigger signal
by a central control processor] event so as to have a first
resolution; and

storing [a plurality of] second data [frames in said
semiconductor buffer memory subsequent to the detection of]
associated with a time period further from said [trigger
signal by said central control processor] event so as to
have a second resolution different than the first
resolution.

16. (Amended) The method of claim 15, further comprising
the step of:

compressing said first data and said second data
[digital video signals] prior to storing said [frames in

said semiconductor buffer memory] first data and said second data.

17. (Amended) The method of claim 16, wherein said compressing [step comprises the step of compressing said digital video signals] is performed with an asymmetric compression routine [prior to storing said frames in said semiconductor buffer memory].

18. (Amended) The method of claim 16, further comprising the step of encrypting said [digital video signals] first data and said second data prior to storing said first data and said second data [frames in said semiconductor buffer memory].

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19. (Amended) The method of claim 15, further comprising the steps of:

storing said data [in said semiconductor buffer memory] at a first [frame] rate prior to [detection of] said event; and

storing said [frames] data [in said semiconductor buffer memory] at a second [frame] rate subsequent to [the detection of] said [trigger] event.

20. (Amended) The method of claim 19, [wherein said second storing step comprises the step of storing said frames in said semiconductor buffer memory at said second frame rate subsequent to the detection of said trigger event,] wherein said second [frame] rate is greater than said first [frame] rate.

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21. (Amended) The method of claim 19, [wherein said second storing step comprises the step of storing said frames in said semiconductor buffer memory at said second frame rate subsequent to the detection of said trigger event,] wherein said second [frame] rate is less than said first [frame] rate.

Please add claims 32-44 as follows:

--32. The device of claim 1, further comprising a tamper resistant housing configured to house the control processor and the memory.--

--33. The device of claim 32, wherein said housing is portable.--

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--34. The device of claim 1, wherein said control processor is further operative to purge the contents of said at least one memory upon user activation of a switch.--

--35. The device of claim 1, wherein the first resolution is exponentially higher than the second resolution.--

--36. The device of claim 1, wherein the control processor is operative to encrypt the data prior to storage in the memory.--

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--37. The device of claim 1, wherein the first and the second resolutions are at least one of temporal resolutions and spatial resolutions.--

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~~--38. The device of claim 1, wherein the first and the second resolutions are spatial resolutions and the control processor is operative to compress the data associated with the portion of the time period closer to an event at a first compression ratio and the data associated with the portion of the time period further from an event at a second compression ratio different than the first compression ratio.--~~

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~~--39. The device according to claim 1, wherein the first and the second resolutions are temporal resolutions and the control processor is operative to store, on a per unit of time basis, more of the data associated with the portion of the time period closer to an event and less of the data associated with the portion of the time period further from an event.--~~

--40. The device of claim 1, wherein the control processor is operative to store only a predetermined amount of data following the event.--

--41. The method of claim 15, further comprising the steps of:

storing said data at the second resolution prior to the event; and

storing said data at the first resolution subsequent to the event.--

--42. The method of claim 15, wherein said first resolution is higher than said second resolution.--

--43. The method of claim 15, wherein the first resolution is exponentially higher than the second resolution.--

Sub B6 --44. A compact portable device for recording data with no moving parts, said recording device comprising:

at least one first sensor type operative to generate data associated with a period of time;

at least one second sensor type operative to generate a signal representing an event;

at least one circular buffer memory for storing the data;

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cont a control processor operative to receive the signal representing the event and to store the data in the at least one circular buffer memory such that the stored data associated with a portion of the time period after receipt of the event signal has a first resolution and the stored data associated with a portion of the time prior to receipt of the event signal has a second resolution lower than the first resolution;

a portable housing configured to house the control processor and the memory; and

at least one connector disposed on said housing for outputting the stored data.--

45. The device of claim 44, further comprising:

a user activated capture switch, wherein the control processor is operative to store only a predetermined amount of data within the at least one circular buffer memory following user activation of the capture switch;

a user activated purge switch, wherein the data stored in the memory is erased following user activation of the purge switch;

a user activated still switch, wherein the control processor is operative to store a single data sample following user activation of the still switch; and

at least one power source for powering the at least one first sensor type, the processor, and the at least one circular buffer memory;

wherein the housing is a tamper resistant housing and is further configured to house the at least one first sensor type.
